



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
 UNITED STATES PATENT AND TRADEMARK OFFICE  
 WASHINGTON, D.C. 20231  
[www.uspto.gov](http://www.uspto.gov)



CONFIRMATION NO. 5286

Bib Data Sheet

|                             |                                   |              |                        |  |
|-----------------------------|-----------------------------------|--------------|------------------------|--|
| SERIAL NUMBER<br>10/016,449 | FILING DATE<br>12/10/2001<br>RULE | CLASS<br>714 | GROUP ART UNIT<br>2133 | ATTORNEY DOCKET NO.<br>RPS920010127US1 |
|-----------------------------|-----------------------------------|--------------|------------------------|--|

**APPLICANTS**

Robert Thomas Bailis, Cary, NC;  
 Charles Edward Kuhlmann, Raleigh, NC;  
 Charles Steven Lingafelt, Durham, NC;  
 Ann Marie Rincon, Burlington, VT;

\*\* CONTINUING DATA \*\*\*\* *None AJSJ*\*\* FOREIGN APPLICATIONS \*\*\*\* *None JJS***IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

\*\* 01/30/2002

|  |   |                        |                     |                    |                         |
|--|---|------------------------|---------------------|--------------------|-------------------------|
| Foreign Priority claimed                                 | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no   | STATE OR COUNTRY<br>NC | SHEETS DRAWING<br>3 | TOTAL CLAIMS<br>15 | INDEPENDENT CLAIMS<br>2 |
| 35 USC 119 (a-d) conditions met                          | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after <i>Allowance</i> |                        |                     |                    |                         |
| Verified and Acknowledged<br><i>Joscelyn G. Cockburn</i> | <i>Joscelyn G. Cockburn</i><br>Examiner's Signature   | <i>JJS</i><br>Initials |                     |                    |                         |

**ADDRESS**

Joscelyn G. Cockburn  
 IBM Corporation 9CCA/B002  
 PO Box 12195  
 Research Triangle Park , NC 27709

**TITLE**

Method and system for use of a field programmable gate array (FPGA) function within an application specific integrated circuit (ASIC) to enable creation of a debugger client within the ASIC

|                            |   |   |
|----------------------------|---|---|
| FILING FEE RECEIVED<br>740 | FEES: Authority has been given in Paper<br>No. _____ to charge/credit DEPOSIT ACCOUNT<br>No. _____ for following: | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )<br><input type="checkbox"/> 1.18 Fees ( Issue )<br><input type="checkbox"/> Other _____<br><input type="checkbox"/> Credit |
|----------------------------|---|---|